


PUNJAB TECHNICAL UNIVERSITY, JALANDHAR
 Result Notification No.:- PTU/Exam/M.Tech.1082012
 M.Tech. (Re-appear) Examination held in Dec-2010
TABULATION SHEET


10% Special Grace Marks Result

College/Institute :-GNDEC, Ludhiana

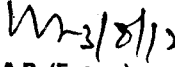
Sr. No	Subject Code	Subject	Subject Type	Int. Marks	Ext. Marks	Result Status	Grace Marks & Subject	Semester	Appeared in Regular/Re-appe
Urollno 96336782163		Name:- Jasmail Singh F.Name:-Mangal Singh		Electrical Engineering (Part Time)					
1	ELE-502	Advanced Power System Analysis	Theory	29	40	Pass			Re-appear

Result revised for complete Degree as per notification no./80 dated 01/08/2012 in this subject
 marks second internal42 in May.2010 & external27 in Dec-2010

Prepared by: 


Checked by: 

Ordinance & Scheme seen, the result
 may be declared and Published Please


 A.R. (Exams)


 Deputy Controller (Exams)

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Page 1

(Theory Internal Marks Out of 50 & External Marks out of 100 & Practical Internal&External Marks out of 50-50 both 100,VLSI Design Internal & External out of 50)

PUNJAB TECHNICAL UNIVERSITY, JALANDHAR
Result Notification No. :- PTU/Exam/M.Tech.19072013

M.Tech. (Re-appear) Examination held in Dec-2010
TABULATION SHEET

40% Special Grace Marks Result

College/Institute :-GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Subject Type	Int. Marks	Ext. Marks	Result Status	Grace Marks & Subject	Semester	Appeared in Regular/Re-appear
Urollno 100377482751		Name:- Shivani Goel F.Name:-Sh.deshmukh Goel		Information Technology (Part Time)					
1	CS-505	Advanced Computer Architecture	Theory	31	40	Pass		1	

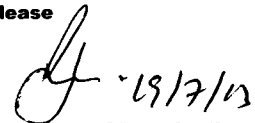
Result revised for complete Degree as per notification no./80 dated 19/07/2013 in this subject
marks second internal 38 in Dec.2010 & external 33 in Dec-2010

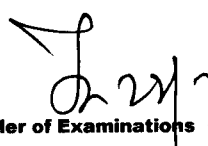
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Page 1

(Theory Internal Marks Out of 50 & External Marks out of 100 & Practical Internal&External Marks out of 50-50 both 100,VLSI Design Internal & External out of 50)

PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

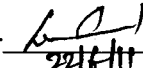
Special Chance Re-evaluation Result (M.Tech.)


Examination held in February-2010


The Result of candidates who applied for re-evaluation is declared as under:-


College name: -GNDEC, Ludhiana

Roll No.	Name	Subject	Result	Grace Marks	Total if Passed
96/EL/197	-----	AI (ELE-513)	36 (Fail)	-----	-----


Prepared By:- 
22/6/11

Checked By:- 

 23/6/11
A.R.(Exam)

 23/6/11

Deputy Controller (Exam)


Controller of Examination

PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

Challenge-evaluation Result Notification No.:- PTU/Exam/M.Tech/
M.Tech. (Challenge-evaluation Result) Examination held in December-2010

TABULATION SHEET

College/Institute :- GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
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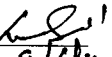
Urollno:- **96478083053** Name:- Pushpinder Singh Grewal F.Name:- Sarbjit Singh Grewal Power Engineerin Full Time

1	PEE-523	Power System Planning	100	40	Pass	1	Pee-523
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A- Means Absent, D-Means Detained
P.R.S. = 'Previous Result Stand'

Ordinance & Scheme seen, the result
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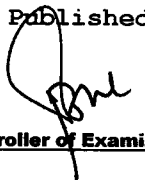
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PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

Challenge-evaluation Result Notification No.:- PTU/Exam/M.Tech/
M.Tech.(Challenge-evaluation Result) Examination held in December-2010

TABULATION SHEET

College/Institute :- GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
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Urollno:- **71404190** Name:- Amandeep Sandhu F.Name:- Jagannath ECE

1	EC-513	Multimedia Comm. Systems	100	40	Pass		
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P.R.S.= 'Previous Result Stand '


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Dy.C.(Exams)


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26 JUL 2011

PUNJAB TECHNICAL UNIVERSITY, JALANDHAR
 Re-evaluation Result Notification No.:- PTU/Exam/M.Tech/
 M.Tech. (Re-evaluation Result) Examination held in December-2010
TABULATION SHEET
College/Institute :- GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 80101107027 Name:- Dharminder Singh F.Name:- S.sher Singh CSE Part Time							
1	CS-517	Parallel Computing	100	35	Re-appear		
Urollno:- 80101637006 Name:- Harmandeep Singh F.Name:- Lakhvir Singh Power Engineerin Part Time							
1	PEE-507	Advanced Mathematics	100	35	Re-appear		
Urollno:- 96336882224 Name:- Ruby F.Name:- Krishan Kumar Gupta ECE Part Time							
1	EC-508	Digital Speech & Image Processing	100	56	Pass		
Urollno:- 96338782375 Name:- Savita Rani F.Name:- Naresh Kumar VLSI Design Part Time							
1	VL-522	ASIC Design and FPGA	50	20	Pass		
Urollno:- 96477383009 Name:- Jagbir Singh F.Name:- Sukhdev Singh Industrial Full Time							
1	IE-503	Operations Management	100	40	Pass		
Urollno:- 96477383018 Name:- Taranjit Singh Virk F.Name:- Sukhdev Singh Industrial Full Time							
1	IE-503	Operations Management	100	40	Pass		
Urollno:- 100376880543 Name:- Ankja Dubey F.Name:- P.b. Dubey ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	44	Pass		
Urollno:- 100376880544 Name:- Cheena Jain F.Name:- Ramesh Kumar Jain ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	42	Pass		
Urollno:- 100376880545 Name:- Gurjeet Singh F.Name:- Amar Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	40	Pass		
Urollno:- 100376880549 Name:- Mandeep Kaur F.Name:- Kulwinder Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	35	Re-appear		
Urollno:- 100376880556 Name:- Amanjot Singh Toor F.Name:- Davinder Singhtoor ECE Full Time							
1	EC-504	Advanced Comm. Systems	100	51	Pass		
Urollno:- 100376880564 Name:- Kiranjot Kaur F.Name:- Satvinder Singh ECE Full Time							
1	EC-504	Advanced Comm. Systems	100	53	Pass		
Urollno:- 100376880565 Name:- Lakhwinder Kaur F.Name:- Bahadur Singh ECE Full Time							
1	EC-504	Advanced Comm. Systems	100	48	Pass		
Urollno:- 100376880567 Name:- Monika F.Name:- Rajpal ECE Full Time							
1	EC-504	Advanced Comm. Systems	100	54	Pass		
Urollno:- 100376880568 Name:- Parminder Kaur F.Name:- Paramjit Singh ECE Full Time							
1	EC-504	Advanced Comm. Systems	100	54	Pass		
Urollno:- 100376880570 Name:- Rajni Bala F.Name:- Vakil Chand ECE Full Time							
1	EC-504	Advanced Comm. Systems	100	43	Pass		
Urollno:- 100376882562 Name:- Kulwinder Singh F.Name:- Balwinder Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	42	Pass		
Urollno:- 100376882566 Name:- Jaideep Kumar Arora F.Name:- Naresh Kumar ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	34	Re-appear		
Urollno:- 100376882569 Name:- Karandeep Singh F.Name:- Harvinder Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	55	Pass		
Urollno:- 100376882574 Name:- Jayant Chawla F.Name:- Kulbhushan Chawla ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	52	Pass		
Urollno:- 100376882581 Name:- Palvi Singh F.Name:- Balvinder Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	40	Pass		
Urollno:- 100376882584 Name:- Harpreet Singh F.Name:- Gurbaksh Singh ECE Part Time							

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Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 100376882584 Name:- Harpreet Singh F.Name:- Gurbaksh Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	40	Pass		
Urollno:- 100376882586 Name:- Parampal Singh F.Name:- Ujjal Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	48	Pass		
Urollno:- 100377380744 Name:- Jasmeet Singh F.Name:- Kulwant Singh Gill Industrial Engg. Full Time							
1	IE-502	Research Methodology	100	43	Pass		
Urollno:- 100377482744 Name:- Khushbeer Kaur Pannu F.Name:- Fill I.T. Part Time							
1	CS-505	Advanced Computer Architecture	100	36	Re-appear		
Urollno:- 100378181022 Name:- Supinderjit Singh F.Name:- Joga Singh Pro. Engg. Full Time							
1	PE-502	Metal Cutting	100	40	Pass		
Urollno:- 100379383110 Name:- Manroop Kaur Gill F.Name:- Balbir Singh Energy Engg. Part Time							
1	ENE-502	Fules and Combustion Technology	100	48	Pass		

A- Means Absent, D-Means Detained
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J 18/5/11
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PUNJAB TECHNICAL UNIVERSITY, JALANDHAR
 Re-evaluation Result Notification No.:- PTU/Exam/M.Tech/
 M.Tech. (Re-evaluation Result) Examination held in December-2010
TABULATION SHEET

College/Institute :- GNDEC, Ludhiana


Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 71404190 Name:- Amandeep Sandhu F.Name:- Jagannath ECE							
1	EC-513	Multimedia Comm. Systems	100	28	Re-appear		
Urollno:- 80101651006 Name:- Menika Seth F.Name:- Mohinder Kapoor ECE Part Time							
1	EC-520	Wireless & Mobile Communication	100	36	Re-appear		
Urollno:- 80101658001 Name:- Amandeep Singh F.Name:- Hari Singh Pro.Engg. Part Time							
1	PE-508	Jig, Fixtures & Die Design	100	40	Pass		
Urollno:- 96336782163 Name:- Jasmail Singh F.Name:- Mangal Singh E.E. Part Time							
1	ELE-502	Advanced Power System Analysis	100	27	Re-appear		
Urollno:- 96336882185 Name:- Mandeep Singh F.Name:- Mandeep Singh ECE Full Time							
1	EC-520	Wireless & Mobile Communication	100	29	Re-appear		
Urollno:- 96336882192 Name:- Sandeep Kaur F.Name:- Sewa Singh ECE Full Time							
1	EC-501	Advanced Mathematics for Engineers	100	20	Re-appear		
Urollno:- 96336882223 Name:- Kashish Sareen F.Name:- Satish Sareen ECE Part Time							
1	EC-501	Advanced Mathematics for Engineers	100	32	Re-appear		
2	EC-502	Electronics System Design	100	36	Re-appear		
Urollno:- 96336883194 Name:- Preeti Sethi F.Name:- S. Paramjit Singh ECE Part Time							
1	EC-513	Multimedia Comm. Systems	100	40	Pass		
Urollno:- 96477383016 Name:- Seetal Singh F.Name:- Rajinder Singh Industrial Full Time							
1	IE-502	Research Methodology	100	42	Pass		
Urollno:- 96477383018 Name:- Taranjit Singh Virk F.Name:- Sukhdev Singh Industrial Full Time							
1	IE-502	Research Methodology	100	28	Re-appear		
Urollno:- 96477383039 Name:- Vinod Kumar F.Name:- Joginder Paul Industrial Part Time							
1	IE-509	Industrial Psychology	100	30	Re-appear		
2	IE-519	Industrial Automation	100	49	Pass		
Urollno:- 96478083053 Name:- Pushpinder Singh Grewal F.Name:- Sarbjit Singh Grewal Power Engineerin Full Time							
1	PEE-523	Power System Planning	100	35	Re-appear		
Urollno:- 96478083064 Name:- Vikas Kumar Pakwaria F.Name:- Darshan Lal Power Engineerin Full Time							
1	PEE-519	Energy Efficient Machines	100	31	Re-appear		
Urollno:- 100376580236 Name:- Ranjeet Singh F.Name:- Kawaljeet Singh CSE Full Time							
1	CS-505	Advanced Computer Architecture	100	58	Pass		
Urollno:- 100376880546 Name:- Hardeep Singh Mankot F.Name:- Sukhpal Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	46	Pass		
Urollno:- 100376880547 Name:- Inderpal Singh F.Name:- Nirmal Pal Singh ECE Part Time							
1	EC-501	Advanced Mathematics for Engineers	100	40	Pass		
Urollno:- 100376880566 Name:- Mohan Lal F.Name:- Tarsem Lal ECE Full Time							
1	EC-502	Electronics System Design	100	34	Re-appear		
Urollno:- 100376880571 Name:- Fill F.Name:- L.r. Kaushal ECE Full Time							
1	EC-501	Advanced Mathematics for Engineers	100	32	Re-appear		
2	EC-502	Electronics System Design	100	32	Re-appear		
Urollno:- 100376882562 Name:- Kulwinder Singh F.Name:- Balwinder Singh ECE Part Time							
1	EC-501	Advanced Mathematics for Engineers	100	40	Pass		
Urollno:- 100376882571 Name:- Gurinderjit Kaur F.Name:- Gulzar Singh Kahlon ECE Part Time							
1	EC-501	Advanced Mathematics for Engineers	100	34	Re-appear		

Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 100377380738 Name:- Amandeep Singh F.Name:- Baldev Singh Industrial Engg. Full Time							
1	IE-502	Research Methodology	100	44	Pass		
Urollno:- 100377380740 Name:- Gurpreet Singh Kaint F.Name:- Jagjit Singh Industrial Engg. Full Time							
1	IE-502	Research Methodology	100	31	Re-appear		
Urollno:- 100377380741 Name:- Ishpreet Singh F.Name:- Mohinder Pal Singh Industrial Engg. Full Time							
1	IE-502	Research Methodology	100	40	Pass		
Urollno:- 100377380752 Name:- Manjeet Singh F.Name:- Mandeep Singh Industrial Engg. Full Time							
1	IE-502	Research Methodology	100	42	Pass		
Urollno:- 100377380756 Name:- Randhir Singh Brar F.Name:- Mohinder Singh Industrial Engg. Full Time							
1	IE-502	Research Methodology	100	40	Pass		
Urollno:- 100377380759 Name:- Sukhdeep Singh F.Name:- Hakam Singh Industrial Engg. Full Time							
1	IE-502	Research Methodology	100	40	Pass		
Urollno:- 100377482737 Name:- Fill F.Name:- Mr. Gian Chand Aneja I.T. Part Time							
1	CS-503	Network Security	100	56	Pass		
Urollno:- 100378080996 Name:- Sukhmider Singh F.Name:- Rajinder Singh Power Engineerin Full Time							
1	PEE-501	Power System Operation & Control	100	42	Pass		
Urollno:- 100378088109 Name:- Mukul Dixit F.Name:- Ramesh Kumar Sharma power Engineerin Full Time							
1	PEE-503	Advanced Power Electronics	100	32	Re-appear		
Urollno:- 100378783085 Name:- Vivek Singh F.Name:- Tarsemlal VLSI Design Part Time							
1	VL-514	VLSI Technology	50	18	Re-appear		
Urollno:- 100379383109 Name:- Harjot Singh F.Name:- Shamsheer Singh Energy Engg. Part Time							
1	ENE-501	Non Conventional Sources of Energy	100	47	Pass		

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P.R.S.='Previous Result Stand'

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PUNJAB TECHNICAL UNIVERSITY, JALANDHAR
 Re-evaluation Result Notification No. :- PTU/Exam/M.Tech/
 M.Tech. (Re-evaluation Result) Examination held in December-2010
TABULATION SHEET

College/Institute :- GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 100378783079 Name:- Deepak Kumar Garg F.Name:- Devinder Kumar Garg VLSI Design Part Time							
1	VL-513	Hardware Description Languages	50	22	Pass		
Urollno:- 100378783085 Name:- Vivek Singh F.Name:- Tarsemlal VLSI Design Part Time							
1	VL-514	VLSI Technology	50	25	Pass		

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PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

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College/Institute :- GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Ext. M.M.	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 100376880563 Name:- Jyotbir Kaur Litt F.Name:- Mr. baldev Singh Litt ECE Full Time							
1	EC-504	Advanced Comm. Systems	100	40	Pass		
Urollno:- 100376882582 Name:- Kirandeep Kaur F.Name:- Karamjit Singh ECE Part Time							
1	EC-504	Advanced Comm. Systems	100	44	Pass		

A- Means Absent, D-Means Detained
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PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

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
Urollno:- **80101652007** Name:- Pardeep Singh F.Name:- Gurmail Singh E.E. Part Time

1	ELE-505	Advanced Electrical Machines	100	27	Re-appear		
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PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

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College/Institute :- GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Int. M.M.	Ext. M.M.	Int. Marks	Internal Result Status	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 96478183065 Name:- Baljinder Singh F.Name:- S.kuldeep Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	41	Pass	58	Pass		
Urollno:- 96478183066 Name:- Chandra Shekhar F.Name:- Mohan Singh Bisht (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	39	Pass	64	Pass		
Urollno:- 96478183067 Name:- Chatwant Singh Pandh F.Name:- Jasbir Singh Pandher (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	41	Pass	64	Pass		
Urollno:- 96478183068 Name:- Gurdeep Singh F.Name:- Harbans Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	41	Pass	56	Pass		
Urollno:- 96478183069 Name:- Gurkamal Nain Singh F.Name:- Resham Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	40	Pass	63	Pass		
Urollno:- 96478183070 Name:- Gurpreet Singh F.Name:- S.iqbal Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	39	Pass	66	Pass		
Urollno:- 96478183071 Name:- Gurwinder Singh F.Name:- Degumber Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	41	Pass	63	Pass		
Urollno:- 96478183072 Name:- Jagdev Singh F.Name:- Jasmail Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	39	Pass	75	Pass		
Urollno:- 96478183073 Name:- Jagtar Singh F.Name:- Daljeet Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	35	Pass	53	Pass		
Urollno:- 96478183074 Name:- Jaswinder Singh F.Name:- Sohan Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	39	Pass	63	Pass		
Urollno:- 96478183075 Name:- Kukinderjit Singh F.Name:- Manjit Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	38	Pass	60	Pass		
Urollno:- 96478183077 Name:- Lakhwinder Singh F.Name:- Harinderjit Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	38	Pass	58	Pass		
Urollno:- 96478183078 Name:- Maninder Pal Singh F.Name:- Parampal Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	36	Pass	59	Pass		
Urollno:- 96478183079 Name:- Manjot Singh Mander F.Name:- Amarjit Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	38	Pass	55	Pass		
Urollno:- 96478183081 Name:- Nipun Sharma F.Name:- Ganesh Sharma (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	37	Pass	63	Pass		
Urollno:- 96478183082 Name:- Pawandeep Singh Grew F.Name:- Jaswinder Singh Grewa (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	36	Pass	71	Pass		
Urollno:- 96478183083 Name:- Rahul Kumar Sharma F.Name:- Joginder Pal Sharma (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	35	Pass	72	Pass		
Urollno:- 96478183084 Name:- Raman Kumar F.Name:- Vijay Kumar (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	37	Pass	65	Pass		
Urollno:- 96478183085 Name:- Satinder Pal Singh F.Name:- Parampal Singh (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	36	Pass	54	Pass		
Urollno:- 96478183086 Name:- Satjot Singh Dhillon F.Name:- Manjit Singh Dhillon (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	35	Pass	64	Pass		
Urollno:- 96478183087 Name:- Shehbaaz Singh Brar F.Name:- Gurdeep Singh Brar (Pro.Engg.) Full Time										
1	PE-521	Metrology & Industrial Inspection	50	100	36	Pass	48	Pass		
Urollno:- 96478183089 Name:- Vipul Sharma F.Name:- R.c Sharma (Pro.Engg.) Full Time										

Sr. No	Subject Code	Subject	Int. M.M.	Ext. M.M.	Int. Marks	Internal Result Status	Ext. Marks	External Result Status	Grace Marks	Grace Subject
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Urol.No:- **96478183089** Name:- Vipul Sharma F.Name:- R.c Sharma (Pro.Engg.) Full Time

1	PE-521	Metrology & Industrial Inspection	50	100	38	Pass	70	Pass		
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A- Means Absent, D-Means Detained

Ordinance & Scheme seen, the result may be declared and Published Please

Published

Prepared by: CC1
17/5/11

Checked by: AS

21/7/11
A.R. (Exams)

17/5/11
D.C.(Exams)

[Signature]
Controller of Examinations

PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

Result Notification No.- PTU/Exam/M.Tech/
M.Tech. Examination held in December-2010

25 APR 2011

TABULATION SHEET

College/Institute :- GNDEC, Ludhiana

Sr. No	Subject Code	Subject	Int. M.M.	Ext. M.M.	Int. Marks	Internal Result Status	Ext. Marks	External Result Status	Grace Marks	Grace Subject
Urollno:- 96338782357 Name:- Parminder Kaur F.Name:- Bhupinder Singh Kalsi (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	27	Pass	20	Pass		
2	VL-523	Memory Design and Testing	50	50	40	Pass	27	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	40	Pass	39	Pass		
Urollno:- 96338782358 Name:- Sanjeev Basra F.Name:- A.k. Basra (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	44	Pass	31	Pass		
2	VL-523	Memory Design and Testing	50	50	42	Pass	39	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	47	Pass	39	Pass		
Urollno:- 96338782359 Name:- Anuj Chawla F.Name:- M.r. Chawla (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	45	Pass	24	Pass		
2	VL-523	Memory Design and Testing	50	50	40	Pass	36	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	46	Pass	42	Pass		
Urollno:- 96338782360 Name:- Munish F.Name:- Jai Gopal (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	45	Pass	26	Pass		
2	VL-523	Memory Design and Testing	50	50	42	Pass	33	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	47	Pass	40	Pass		
Urollno:- 96338782361 Name:- Anil Koul F.Name:- B.n. Koul (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	33	Pass	28	Pass		
2	VL-523	Memory Design and Testing	50	50	40	Pass	28	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	44	Pass	37	Pass		
Urollno:- 96338782362 Name:- Jagpal Singh F.Name:- S. Kuldip Singh (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	38	Pass	21	Pass		
2	VL-523	Memory Design and Testing	50	50	38	Pass	26	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	46	Pass	35	Pass		
Urollno:- 96338782363 Name:- Vishaka Ambardar F.Name:- Tej Kumar Ambardar (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	31	Pass	24	Pass		
2	VL-523	Memory Design and Testing	50	50	28	Pass	29	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	35	Pass	38	Pass		
Urollno:- 96338782364 Name:- Tejinder Singh F.Name:- Gurnam Singh (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	39	Pass	25	Pass		
2	VL-523	Memory Design and Testing	50	50	39	Pass	29	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	36	Pass	39	Pass		
Urollno:- 96338782365 Name:- Narender Kumar F.Name:- Mahant Ram (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	42	Pass	24	Pass		
2	VL-523	Memory Design and Testing	50	50	41	Pass	27	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	48	Pass	32	Pass		
Urollno:- 96338782366 Name:- Ramandeep Singh F.Name:- Baldev Singh (VLSI Design) Part Time										
1	VL-515	Design of Analog/Mixed Mode VLSI Circuits	50	50	-----		9	Re-appear		
2	VL-522	ASIC Design and FPGA	50	50	39	Pass	21	Pass		
3	VL-523	Memory Design and Testing	50	50	36	Pass	30	Pass		
4	VL-527	Designing with FPGA's - LAB	50	50	44	Pass	36	Pass		
Urollno:- 96338782367 Name:- Gurpreet Kaur Bedi F.Name:- Surinder Singh Bedi (VLSI Design) Part Time										
1	VL-522	ASIC Design and FPGA	50	50	35	Pass	20	Pass		

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Sr. No	Subject Code	Subject	Int. M.M.	Ext. M.M.	Int. Marks	Internal Result Status	Ext. Marks	External Result Status	Grace Marks	Grace Subject
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Urollno:- **96338782367** Name:- Gurpreet Kaur Bedi F.Name:- Surinder Singh Bedi (VLSI Design) Part Time

2	VL-523	Memory Design and Testing	50	50	40	Pass	31	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	44	Pass	38	Pass		

Urollno:- **96338782368** Name:- Anuradha F.Name:- Rajesh Kumar (VLSI Design) Part Time

1	VL-522	ASIC Design and FPGA	50	50	29	Pass	27	Pass		
2	VL-523	Memory Design and Testing	50	50	39	Pass	23	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	42	Pass	34	Pass		

Urollno:- **96338782369** Name:- Maninder Singh F.Name:- Harvinder Singh (VLSI Design) Part Time

1	VL-522	ASIC Design and FPGA	50	50	23	Pass	11	Re-appear		
2	VL-523	Memory Design and Testing	50	50	34	Pass	16	Re-appear		
3	VL-527	Designing with FPGA's - LAB	50	50	35	Pass	30	Pass		

Urollno:- **96338782370** Name:- Amritansh Saini F.Name:- Amarjit Singh (VLSI Design) Part Time

1	VL-522	ASIC Design and FPGA	50	50	28	Pass	23	Pass		
2	VL-523	Memory Design and Testing	50	50	36	Pass	34	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	35	Pass	38	Pass		

Urollno:- **96338782371** Name:- Ravinder Singh F.Name:- Surinder Singh (VLSI Design) Part Time

1	VL-512	VLSI Design concepts	50	50	-----		30	Pass		
2	VL-513	Hardware Description Languages	50	50	24	Pass	A	Re-appear		
3	VL-522	ASIC Design and FPGA	50	50	27	Pass	28	Pass		
4	VL-523	Memory Design and Testing	50	50	33	Pass	24	Pass		
5	VL-527	Designing with FPGA's - LAB	50	50	35	Pass	39	Pass		

Urollno:- **96338782372** Name:- Jaspreet Singh Mander F.Name:- Harshan Singh Mander (VLSI Design) Part Time

1	VL-515	Design of Analog/Mixed Mode VLSI Circuits	50	50	-----		7	Re-appear		
2	VL-522	ASIC Design and FPGA	50	50	26	Pass	6	Re-appear		
3	VL-523	Memory Design and Testing	50	50	32	Pass	30	Pass		
4	VL-527	Designing with FPGA's - LAB	50	50	35	Pass	32	Pass		

Urollno:- **96338782373** Name:- Kiranpreet Singh F.Name:- Gurdeep Singh (VLSI Design) Part Time

1	VL-514	VLSI Technology	50	50	-----		24	Pass		
2	VL-515	Design of Analog/Mixed Mode VLSI Circuits	50	50	-----		9	Re-appear		
3	VL-522	ASIC Design and FPGA	50	50	36	Pass	20	Pass		
4	VL-523	Memory Design and Testing	50	50	36	Pass	24	Pass		
5	VL-527	Designing with FPGA's - LAB	50	50	45	Pass	39	Pass		

Urollno:- **96338782374** Name:- Tanupreet Sabharwal F.Name:- Gurvinder Singh Sabharwal (VLSI Design) Part Time

1	VL-522	ASIC Design and FPGA	50	50	33	Pass	26	Pass		
2	VL-523	Memory Design and Testing	50	50	41	Pass	32	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	43	Pass	39	Pass		

Urollno:- **96338782375** Name:- Savita Rani F.Name:- Naresh Kumar (VLSI Design) Part Time

1	VL-522	ASIC Design and FPGA	50	50	30	Pass	18	Re-appear		
2	VL-523	Memory Design and Testing	50	50	37	Pass	28	Pass		
3	VL-527	Designing with FPGA's - LAB	50	50	44	Pass	38	Pass		

Urollno:- **96338782376** Name:- Jagjeet Singh F.Name:- Daljit Singh (VLSI Design) Part Time

1	VL-515	Design of Analog/Mixed Mode VLSI Circuits	50	50	-----		10	Re-appear		
2	VL-522	ASIC Design and FPGA	50	50	26	Pass	16	Re-appear		
3	VL-523	Memory Design and Testing	50	50	37	Pass	29	Pass		
4	VL-527	Designing with FPGA's - LAB	50	50	35	Pass	35	Pass		

Urollno:- **96338782377** Name:- Varun Gautam F.Name:- Anil Kumar Gautam (VLSI Design) Part Time

1	VL-522	ASIC Design and FPGA	50	50	30	Pass	21	Pass		
2	VL-523	Memory Design and Testing	50	50	36	Pass	35	Pass		