

PUNJAB TECHNICAL UNIVERSITY, JALANDHAR



Bachelor of Technology 5th Semester (Re-appear), Examination held in March-2012

GNDEC, Ludhiana

Branch: **Electronics & Communication Engg.**

Special Chance **Result Notification No.PTU/DEX/754/9/08**

Sr. No	Roll No. Name Father's Name	Analog Comm. System Theory EC-301 Int(40)/Ext(60)	Antenna & Wave Propa. Theory EC-303 Int(40)/Ext(60)	Linear Integrated Circuits Theory EC-305 Int(40)/Ext(60)	Microprocessor & Its Appl. Theory EC-307 Int(40)/Ext(60)	Pulse, Digital & Switching Cts. Theory EC-309 Int(40)/Ext(60)	Total Quality Mgt. Theory ME-251 Int(40)/Ext(60)	Analog Comm. System (Lab-VII) Practical EC-311 Int(30)/Ext(20)	Linear Integrated Circuits (Lab-VIII) Practical EC-313 Int(30)/Ext(20)	Microprocessors Lab-IX Practical EC-315 Int(30)/Ext(20)	Industrial Training Practical IT Int(60)/Ext(40)	TotalMarks / Re-appear in Paper(s) Max. Marks 850	Subject Code G.M										
GNDEC, Ludhiana																							
1	314042928 Hardeep Singh Charan Singh	18	24	17	8	17	24	18	24	19	28	16	27	16	15	18	10	18	12	28	32	RP-EC303	

May be declared and published Please

Publish

AR. (Exam)

Controller of Examinations

Page 1 "RP"means rep.in Ext
"RT"means rep.in Both INT. & Ext Papers
"RTI"means rep.in Int.
"x"means awards not recieved from college

"D"means Detained,
"A"means Absent,
"RL" means result late

Prepared by: _____

Checked by:1 _____

2. _____