

# PUNJAB TECHNICAL UNIVERSITY, JALANDHAR

Bachelor of Technology 3rd Sem. (Re-appear) Examination held in May-2011  
 Re-Evaluation Result Examination No. PTU/Exams/754

College: GNDEC, Ludhiana

Electrical Engineering

Sr. No.	Roll No. Name Fname	Applied Maths-III		Network analysis & Synthesis		Object Oriented Programming		Mag. Cirt. & Trans.		Ele. Measur. & Measuring Inst.		Electronics Devices & Circuits		Lab III (Objected Oriented Prog.)		Lab-II (EM & MI)		Lab-I (ED & NW)		W.Training		Total / Re-appear(s) Max Marks=850	G.M	Subject Code	
		AM-201 T2011 (40/16)	T201E (60/24)	EE-201 T2011 (40/16)	T201E (60/24)	CS-252 T2521 (40/16)	T252E (60/24)	EE-203 T2031 (40/16)	T203E (60/24)	EE-205 T2051 (40/16)	T205E (60/24)	EE-207 T2071 (40/15)	T207E (60/24)	CS-254 P2541 (30/12)	P254E (20/8)	EE-211 P2111 (30/12)	P211E (20/8)	EE-209 P2091 (30/12)	P209E (20/8)	EE-213 P2131 (60/24)	P213E (40/16)				
	80101109092 Swarnjit Singh Surjit Singh	20	24	27	41	28	24	26	29	25	39	23	27	20	13	19	13	20	11	45	32	201227	506		

Published

May be declared and Published Please.

Ma 18/10/12  
 A.R.(Exams)

18/10/12  
 Controller of Examinations

RT=Re-appear jin both internal & external papers  
 RP=Re-appear in external papers only.  
 RTI=Re-appear in internal papers only.  
 "D" means Detained,  
 "Ab" means Absent,  
 "RL" means Result Page 1

Ordinance and scheme seen

Prepared by: PS 18/10/12  
 Checked by: 1. RS  
 2. \_\_\_\_\_