

COURSE OBJECTIVE

Low Power has emerged as a Principal theme in today's electronics industry. With the remarkable success and growth of the class of personal computing devices (portable desktops, audio- and video-based multimedia products) and wireless communications systems (personal digital assistants and personal communicators), demand of high-speed computation and complex functionality with low power consumption has increased. In the past, the major concerns of the designer were area, performance, cost and reliability; power consideration was mostly of only secondary importance. In recent years, however, this has begun to change and power is being given comparable weight along with area and speed considerations. Thus, the need for low power has caused a major paradigm shift where power dissipation has become as important a consideration as performance and area.

Many issues are faced by designers at architectural, logic, circuit and device levels which are required to be overcome by efficient techniques. This course will address the latest trends and future challenges that must be met to design low power, high performance electronic systems.

COURSE CONTENTS

- ◆ Low power high performance electronic systems
- ◆ Design and Analysis of Antennas for low power applications
- ◆ Low power VLSI circuits & systems
- ◆ Low power electronics for Signal & Image Processing
- ◆ Wireless & Optical communication for low power applications
- ◆ Hands-on sessions on relevant tools

IMPORTANT DATES

Submission of Registration form by 05.03.2018

Acceptance Notification by 07.03.2018

DATE & VENUE FOR REGISTRATION

12.03.2018 (9AM) at Testing & Consultancy Cell (TCC) Seminar Hall, GNDEC, Ludhiana

CONTACT DETAILS

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Professor & Head

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PATRON

Dr. Sehijpal Singh (Principal)

Guru Nanak Dev Engineering College, Ldh.

COORDINATOR

Dr. Sandeep Singh Gill
(HOD ECE)

ORGANISING COMMITTEE

Prof. Ameeta Seehra
Prof. Narwant Singh
Dr. Balwinder Singh
Dr. Munish Rattan
Dr. Baljeet Kaur
Prof. Harminder Kaur
Prof. Gurjot Kaur
Prof. Navneet Kaur
Prof. Gurburneet Kaur

Prof. Chahat Jain
Prof. Kuldeepak Singh
Prof. Harminder Kaur Aulakh
Prof. Jasmeet Kaur
Prof. Daljit Singh
Prof. Sapanjot Kaur
Prof. Simranjit Kaur
Prof. Naazia Makkar
Prof. Nishu Kansal

ONE WEEK QIP SHORT TERM COURSE ON

*"Design Issues
in*

*Low Power Applications
in Electronics"*

(12th-16th March, 2018)



COORDINATOR

Dr. Sandeep Singh Gill

ORGANISED BY

Department of

**ELECTRONICS & COMMUNICATION
ENGINEERING**

Guru Nanak Dev Engineering College

Gill Road, Ludhiana, Punjab-141006

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ABOUT THE INSTITUTE

Guru Nanak Dev Engineering College (An Autonomous College under UGC Act), established in 1956, has been striving persistently in the field of education by comprehending the need for upliftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi on 17.8.2012. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various accredited graduate level B.Tech courses, the institute also imparts thirteen Postgraduate courses both on regular and part time basis. An excellent platform is provided to the researchers leading to Ph.D degree program.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering, established in 1981, is one of the most dynamic departments of Guru Nanak Dev Engineering College. It was the first diversification initiative by the college, 25 years after its establishment. The department is currently running NBA Accredited UG and PG courses in Electronics and Communication Engineering and has around 15 scholars enrolled for doctorate in areas like Antenna Design, VLSI, Optical communication etc. Ever since its inception, the department has been the hub of academic excellence through some great teachers who have spread their wings all over the globe. The alumni of the department are not only excelling in India but also in the Silicon Valley and other hubs of Electronics Technology. They are at the forefront of the Telecom revolution of the last 20 years and manning pivotal positions in Telecom operators in India, South East Asia, Pacific region and Africa. In the last five years, as a part of an academically autonomous system, the department

has taken numerous quality initiatives including revamping of syllabi so that it is in tune with the outcome based system as proposed in the Washington Accord. Both its courses viz. B.Tech (ECE) & M.Tech (ECE) are accredited by NBA under Tier-I. The department is having very good pass percentage usually above 95% for UG/PG students. ECE Department is continuously maintaining strong International collaboration with outside world in form of joint research publications, editorial board memberships of reputed International journals.

REGISTRATION

There is no registration fee for participants from academic institutes. All short-listed candidates are required to confirm their participation by sending the scanned copy of duly filled Registration form by Email to qipec@gmail.com. Shortlisting and confirmation of eligible candidates will be on a first come first served basis. Incomplete application forms will not be entertained. Registration forms should reach the coordinator by 05.03.2018.

ELIGIBILITY

Faculty members of degree level engineering colleges recognized by AICTE are eligible to attend the course. Applications from various fields of engineering and other related disciplines are

TRANSPORT BOARDING & LODGING

Participants are entitled for II class (Sleeper Class) or AC railway fare to and fro by the shortest route from college to GNDEC, Ludhiana. Accommodation will be provided in the students Hostels on sharing basis.

ONE WEEK QIP SHORT TERM COURSE ON "Design Issues in Low Power Applications in Electronics" (12th-16th March, 2018)

REGISTRATION FORM

Name (in block letters):

Gender:

Qualification:

Designation/Department:

Organization:

Address:

Mobile:

Email :

Accommodation Required in Campus: YES / NO

Signature of Participant

Sponsorship & Signature of Head of the College / Institute (with date & seal).

(Important: By signing above, head of the College/Institute certifies that applicant is a faculty member of degree level engineering college recognized by AICTE)