

RAECT - 13 CONFERENCE SCHEDULE

DAY 1 (21ST MARCH 2013)

EVENTS	TIMINGS	VENUE
REGISTRATION	9:00A.M. ONWARDS	OUTSIDE COLLEGE AUDITORIUM
INAUGURAL SESSION	10:30A.M.-11:30A.M.	COLLEGE AUDITORIUM
TEA	11:30A.M.-12.00P.M.	OUTSIDE COLLEGE AUDITORIUM
KEYNOTE ADDRESS	12:00P.M.-12:30P.M.	CONSULTANCY HALL
SESSION 1 (VLSI DESIGN)	12:30P.M.-2:00P.M.	CONSULTANCY HALL
SESSION2 (WIRELESS COMMUNICATION & SOFT COMPUTING AND OPTIMIZATION)	12:30P.M.-2:00P.M.	WORKSHOP SEMINAR HALL
LUNCH	2:00P.M.-2:45P.M.	ALUMNI HOME
SESSION 1(CONTD.)	2:45P.M.-5:00P.M.	CONSULTANCY HALL
SESSION 2(CONTD.)	2:45P.M.-5:00P.M.	WORKSHOP SEMINAR HALL
SUFI EVENING (JANAB SARDAR ALI AND PARTY)	6:00P.M.-8:00P.M.	COLLEGE AUDITORIUM
DIRECTORS' CONFERENCE DINNER	8:30P.M. ONWARDS	ALUMNI HOME

DAY 2 (22ND MARCH 2013)

EVENTS	TIMINGS	VENUE
SESSION 3 (OPTICAL FIBRE COMMUNICATION)	9:30A.M.-11A.M.	CONSULTANCY HALL
SESSION 4 (SIGNAL PROCESSING)	9:30A.M-11:00A.M.	WORKSHOP SEMINAR HALL
TEA	11:00A.M.-11:15A.M.	
SESSION 3 (CONTD.)	11:15A.M.-12:00P.M.	CONSULTANCY HALL
SESSION 4(CONTD.)	11:15A.M.-12:00P.M.	WORKSHOP SEMINAR HALL
SESSION 5 (MICROWAVE ANTENNA AND EMBEDDED SYSTEMS)	12:00P.M.-2:00P.M.	CONSULTANCY HALL
LUNCH	2:00 PM	ALUMNI HOME

SESSION-1(VLSI DESIGN)

Sr. No.	Paper ID	Paper Title	Name of authors with contact details	Remarks
1.	VLSI 01	Power Efficient Adiabatic 2:1 MUX	¹ Yashika Thakur, ² Sonika Mahajan, ³ Rajesh Mehra ^{1, 2} M. E. Student , Electronics & Communication Department, NITTTR, Sector 26 Chandigarh, India ³ Associate professor (E.C.E), Sector 26 Chandigarh, India, Email: rajeshmehra@yahoo.com, rajesh@nitttrchdac.in ¹ yashika04thakur@gmail.com ² sonikashelly@gmail.com	
2.	VLSI 02	Effect of Temperature on Noise in Dynamic CMOS Circuits in Super Threshold Region	Bishwajeet Kumar, Rajeevan Chandel and Rohit Dhiman	
3.	VLSI 03	Automation of HDL and C++ Code Generation for Pipelined and Multiplier Free 2D-CWT Image Compression Algorithm	Tanupreet Sabharwal ¹ , Munish Rattan ² ^{1,2} Department of Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab, India ¹ cecm.ece.tps@gmail.com , ² rattanmunish@gndec.ac.in	
4.	VLSI 04	characterisation of MISSFET structure for performance enhancement	Tarun Chaudhary ¹ , Gargi Khanna ^{2,1} Department of Electronics and Communication Engineering National Institute of Technology, Hamirpur (HP), India ¹ taru_choadhary@yahoo.co.in ² gargikhanna20@gmail.com	
5.	VLSI 05	Thermal Management Technologies for Power Electronics	Parminder Kaur* *Student, M.Tech (VLSI Design), Guru Nanak Dev Engineering College, Ludhiana E.mail: param.jas@gmail.com	
6.	VLSI 06	Multi-Objective Approach for Power, Delay Optimization Using GA	Dr. Sandeep Singh Gill, Gurpreet Kaur Bedi(Working as an Assistant Professor in RIMT- IET Engineering College, Mandi- Gobindgarh), Gurpreet_er18@yahoo.in	

7.	VLSI 07	A Comparative Study of Silicon Quantum Well Tunnelling MOSFET and Conventional MOSFET	Sanjeev Kumar , Gargi Khanna National Institute of Technology , Hamirpur(H.P)	
8.	VLSI 08	Design and implementation of Floating Point Unit on Hybrid FPGAs	Savita Rani Department of Electronics and Communication Engineering, Guru Nanak Dev Engineering College, Ludhiana, Punjab, India savitasharma723@gmail.com	
9.	VLSI 09	CARBON NANO TUBE FIELD EFFECT TRANSISTOR BASED FULL ADDER CELL	Cheena Jain Department of Electronics and Communication Engineering, Guru Nanak Dev Engineering College Ludhiana, Punjab cheenaace@gmail.com	
10.	VLSI 10	High speed , low power FIR filter design : Review	Tejinder Singh ¹ (M.Tech(VLSI Design), GNDEC, Ldh. tjndrsngh5@gmail.com Balwinder Singh Dhaliwal ² Astd. Prof.(ECE) GNDEC, Ldh. <u>bsdhaliwal@ymail.com</u> Anita ³ Astd. Prof.(ECE) .BUEST, Baddi eranita5@gmail.com	
11.	VLSI 11	Systolic Array Architectures for FFT: A Review	¹ Prabhjot Singh and ² Baljeet Kaur ¹ <i>M.Tech Student (VLSI Design), Guru Nanak Dev Engineering College, Ludhiana</i> ² <i>Assistant Professor, Department of Electronics & Communication Engineering, Guru Nanak Dev Engineering College, Ludhiana</i> Email: singhprabhjot8@gmail.com, baljeetkaur@gndec.ac.in	
12.	VLSI 12	Overview of Reversible Logic Gates	Manpreet Kaur M-Tech(VLSI Design) manpreetmoney21@yahoo.com ECE Department Balwinder Singh Dhaliwal Guru Nanak Dev Engineering College Ludhiana (141006), India bsdhaliwal_79@yahoo.co.in	

13.	VLSI 13	A Review of Optimization Techniques for VLSI Floorplanning	Amarjot Kaur , Guru nanak Dev Engineering College Ludhiana, India amarjotak@rediffmail.com Sandeep Singh Gill Guru nanak Dev Engineering College Ludhiana ,India ssg@gndec.ac.in	
14.	VLSI 14	Design and Analysis of Semiconductor Devices using TCAD Tools	Sonal Shreya, Raghvendra P. Varma, Anup Kumar, Anil and Rajeevan Chandel	
15.	VLSI 15	A Review On Reversible Logic Circuits	Taranjot Kaur Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India taranjotkaur1990@yahoo.com Komal Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India komal_chand88@yahoo.com Nirmaljeet Singh Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India nirmaljeet@rediffmail.com	
16.	VLSI 16	Analysis of 6t CMOS SRAM cell in 180nm technology	Rohin Gupta ¹ , S. S. Gill ² , Navneet Kaur ² ¹ M. Tech. Student, ² Faculty, Department of Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab, India engg.rohin@gmail.com, ssg@gndec.ac.in, er.navneet_15@yahoo.in	

17.	VLSI 17	A naive design approach for low power 16 bit Full Adder Cell	Rajinder Singh Dept. Of electronic & communication G.G.S.C.E.T Talwandi Sabo, Punjab Jaspreet Singh Dept. Of electronic & communication G.G.S.C.E.T Talwandi Sabo, Punjab Jagbir Singh Dept. Of electronic & communication G.G.S.C.E.T Talwandi Sabo, Punjab	
18.	VLSI 18	A Review on Network on Chip: An On-Chip Interconnection Network	Jasmeet Kaur ¹ , Mohit Gupta ² , Gurpurneet Kaur ³ , Dr. Sandeep Singh Gill ⁴ Electronics and Communication Engineering Department Guru Nanak Dev Engineering College, Ludhiana, India ¹ Jasmeetkr.90@gmail.com, ² mgrocks170@gmail.com, ³ gita_taur@yahoo.co.in, ⁴ ssg@gndec.ac.in	
19.	VLSI 19	Silicon Nanowire Double Gate FETs & Its Applications: A Review	Sanjeev Kumar ¹ , Simrandeep Singh ² , Gurpurneet Kaur ³ , Dr. Sandeep Singh Gill ⁴ ^{1,2} Mtech student :Department of Electronics and Communication Engineering ^{3,4} Assistant professor: Department of Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab, India ¹ sk11092@gmail.com	
20.	VLSI 20	LOW POWER TIQ FLASH A/D CONVERTER	Jaspreet Kaur ¹ , Amit Kumar ² , Dr. Munish Rattan ³ ¹ M.Tech (VLSI), GNDEC (Ludhiana) ² Asst. Professor, ECE Department, G.I.T.M (Karnal) ³ Professor, ECE Department, GNDEC, Ludhiana	
21.	VLSI 21	Comparative Study of Various Methods Used To Design Arithmetic &	Taranjot Kaur Electronics & Communication Engineering Guru Nanak Dev Engineering	

		Logic Unit Using Reversible Logics	College, Ludhiana Punjab, India taranjotkaur1990@yahoo.com Komal Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India komal_chand88@yahoo.com Nirmaljeet Singh Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India nirmaljeet@rediffmail.com	
22.	VLSI 23	A Review on Devices beyond sub-22nm node	¹ Honish Kumar, ² Mohit Gupta,³ Simrandeep singh, ⁴ Dr S.S.Gill, ³ Pf. Navneet Kaur ^{1,2,3} M. Tech. Student, ⁴ Associate Professor, ⁵ Assistant Professor, Electronics & Communication Department, GNDEC, Ludhiana, India. ¹ honish.12@gmail.com, ² mgrocks170@gmail.com,³ sdsingh092@gmail.com,⁴ ssg@gndec.ac.in, ⁵ er.navneet_15@yahoo.co.in	
23.	VLSI 24	Performance of the InAlAs/InGaAs HEMT with different gate lengths and their use in wave application: A Review	Simrandeep Singh¹, Sanjeev Kumar², Honish kumar³, Dr. Sandeep Singh Gill³ ^{1,2} Mtech Student: Department of Electronics and Communication Engineering Assistant professor: Department of Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab, India ¹ sdsingh092@gmail.com	
24.	VLSI 25	A Review on FinFET Characterstics and its Applications	Mohit Gupta¹, Jasmeet Kaur², Honish kumar³, Navneet Kaur⁴, Dr. Sandeep Singh Gill⁵ Electronics and Communication Engineering Department	

			Guru Nanak Dev Engineering College, Ludhiana, India ¹ mgrocks170@gmail.com, ² jasmeetkr.90@gmail.com, ³ honish.12@gmail.com, ⁴ er.navneet_15@yahoo.in, ⁵ ssg@gndec.ac.in	
25.	VLSI 26	High Speed Domino logic design for NAND Gate	Er. Shivani Sharma Er. Yogesh Garg Er.Neha Singh Department of ECE Department of ECE Department of ECE GPCG Patiala GPCG Patiala CCE Mohalii	
26.	VLSI 27	A Review on Leakage Power Control Techniques in VLSI	Navneet Kaur ¹ , Sandeep Singh Gill ² , Gurpurneet Kaur ³ , Chahat Jain ⁴ , Gurjot Kaur Walia ⁵ ^{1,3,4,5} Assistant Professor, Department of Electronics and Communication, Associate Professor, Department of Electronics and Communication Guru Nanak Dev Engineering College, Ludhiana (Punjab) ¹ er.navneet_15@yahoo.co.in, ² ssg@gndec.ac.in, ³ gita_taur@yahoo.co.in, ⁴ chahatjain26@gmail.com, ⁵ gurjotwalia@yahoo.com	
27.	VLSI 28	Cross-Talk Reduction in Global VLSI Interconnects	Gargi Khanna E&CE Dept., NIT Hamirpur Hamirpur, India gargikhanna20@gmail.com Rajeevan Chandel E&CE Dept., NIT Hamirpur Hamirpur, India rajeevanchandel@gmail.com Ashwani Chandel EEE Dept., NIT Hamirpur Hamirpur, India ashchandelin@gmail.com	
28.	VLSI 29	A Review on VLSI Interconnect	Gurpurneet Kaur ¹ , Chatar pratap Singh ² , Munish Rattan ³ , Navneet Kaur ⁴ , Chahat Jain ⁵ ^{1, 4, 5} Assistant Professor, Department of Electronics and Communication, ³ Associate Professor, Department of Electronics and Communication	

			Guru Nanak Dev Engineering College, Ludhiana (Punjab) ² Lecturer, Department of Electronics and Communication, Baba Banda Singh Bahadur Polytechnic College, Fatehgarh Sahib (Punjab) ¹gita_taur@yahoo.co.in	
29.	VLSI 30	Reduction of Gate Induced Drain Leakage Current in Short channel SOI MOSFET	Amit Kumar Keshari and Gargi Khanna, Member, IETE	
30	VLSI 31	Semi-Dynamic Flip-Flops with Skew Tolerance	Gurinderjit kaur	

SESSION-2(WIRELESS COMMUNICATION AND SOFT COMPUTING AND OPTIMIZATION)

Sr. No.	Paper ID	Paper Title	Name of authors with contact details	Remarks
1.	WC 01	Enhancement of Prevailing IB-LEACH for Heterogeneous Wireless Sensor Networks	Yogesh Kumar Student, M.TECH ECE Department, Guru Nanak Dev Engg. College, Ludhiana, India, kumar_yogesh1087@rediffmail.com Kanwaljit Singh Associate Professor ECE Department, Guru Nanak Dev Engg. College Ludhiana, India kjitsingh@gndec.ac.in Yogyata Jain Assistant Professor CSE Department, IET Bhaddal, Ropar, India Yogyatagupta2010@gmail.com	
2.	WC02	CENTROID LOCALISATION FOR WIRELESS SENSOR NETWORKS	Kashish Sareen #GURUNANAK DEV ENGINEERING COLLEGE, LUDHIANA (Pursuing M.Tech) kashishsareen@yahoo.co.in Kanwaljit Singh Associate Professor, GURUNANAK DEV ENGINEERING COLLEGE, LUDHIANA kjitsingh@gmail.com	
3.	WC 03	GSM Technology based hot line inspection robot for fault diagnosis and maintenance of overhead transmission line.	Asst. Professor Ankita Gupta Department of Electrical and Electronics Engineering, Gian Jyoti Group of Institutions Shambu Kalan, Punjab, India ankita123.gupta@gmail.com Asst. Professor Harmeet Singh Gill Department of Electrical Engineering, Guru Nanak Dev Engineering College Ludhiana, Punjab, India meetigill@gmail.com	
4.	WC 05	EPON and WiMAX – the future Fiber Wireless (FiWi) networks	Anita Rana ¹ , Gaurav Soni ² , , Amarjit Kaur ³ ² Associate professor , Department of Electronics and Communication Engineering, ACET,Amritsar ^{1,3} M-tech Scholar, Department of Electronics and Communication Engineering, ACET, Amritsar	

5.	WC 06	OFDM for Wireless Communication	RichaBhatia ,Assistant Professor,AIACTR ,richatimes@gmail.com NehakumariBhardwaj,student, AIACTR,nehakbhardwaj98@gmail.com	
6.	WC 07	A Review of Underwater Wireless Communication	Deepjyoti , Jasdeep Kaur (M.Tech pursuing) Guru Nanak Dev Engineering College , Ludhiana	
7.	WC 08	A Comparative analysis of Adhoc Networks based protocols	Simerpreet Singh A.P., ECE Deptt. BGIET, Sangrur	
8.	WC 09	ANALYSIS OF HANDOVER SCHEMES IN WiMAX	Pawandeep Kaur Gill#, #M.Tech Students, Electronics and Communication Engg. Deptt , Guru Nanak Dev Engineering College Punjab-141006 ,India. Ameeta seehra* *Associate Professor, Electronics and Communication engg.Deptt, Guru Nank Dev Engineering College Punjab-141006 ,India. gillpawandeep26@yahoo.com, am_seehra@yahoo.co.in	
9.	WC 10	Design of circular fractal patch antenna for Wi-Max applications	Manu Bhatia M.Tech Student GNDEC Ludhiana Ludhiana ,India manu7m@gmail.com Balwinder Singh Dhaliwal Assistant Professor GNDEC Ludhiana ,India bsdhaliwal@ymail.com	
10.	WC 11	A REVIEW OF ZONE ROUTING PROTOCOLS FOR MOBILE AD HOC NETWORKS	Sonali Rajput, Prabhjeet kaur, V K Banga Amritsar College of Engineering & Technology, Amritsar, Punjab, INDIA. s.rajput1193@yahoo.com , Prabhjeetkaur9@yahoo.com, vijaykumar.banga@gmail.com	
11.	WC 12	A Comparative Study of Wireless Home Automation Technologies	Ms. Harneet kaur (harneet_kooner@yahoo.com) Student at U.I.E.T, PU, Chandigarh Ms. Sukesha Sharma (er_sukesha@yahoo.com) Asst. Professor at U.I.E.T, PU, Chandigarh	
13.	WC 13	A Survey of AODV under Black Hole Attack	Mr. Madan Lal 1, Rasneet Kaur 2 1 Asst. Prof., Computer Engineering Department, University College of Engineering	

			Punjabi University, Patiala, Punjab, India 2 Computer Engineering Department, University College of Engineering Punjabi University, Patiala, Punjab, India	
12.	WC 14	Review Paper on Measures to Counter Denial-of-Service (DoS) Attacks in VoIP	Jaspreet Kaur, Kritika Prasher M.Tech pursuing Guru Nanak Dev Engineering College, Ludhiana	
13.	WC 15	The Evolution to 4G Technologies	SavroopKaur ,HarleenKaur , RaghavPuri M.tech (2 nd sem ECE students) GIMET,Amritsar,Punjab	
14.	WC 17	Services provided by network security in networking	Tejasvit kaur tanmeet kaur chandigarh university(gharuan)	
15.	WC 18	A Review of Cognitive Radio Spectrum Sensing Techniques	Sobneek Kaur Guru Nanak Dev Engineering College Ludhiana, Punjab, India sobneek@gmail.com Dr. Munish Rattan Guru Nanak Dev Engineering College Ludhiana, Punjab, India rattanmunish@gndec.ac.in	
16.	WC 19	Review of Challenges in Accessing Cloud Services through mobile devices	Ranjodh Singh, Sukhinder Preet Singh , Amrinder Singh, Rahul	
17.	WC 20	Evolution in Mobile Communication Technology- A Review	Harminder Kaur ^{#1} , Gurjot Kaur Walia ^{#1} , Jagjeet Kaur ^{#2} , Navandeep Singh Tung ^{#3} #1 Assistant Professor, ECE Department, Guru Nanak Dev Engineering College, Ludhiana, Punjab, INDIA #2 Assistant Professor, Production Department, Guru Nanak Dev Engineering College, Ludhiana, Punjab, INDIA #3 Student, M.Tech(Mechanical), UCoE, Punjabi University, Patiala, Punjab, INDIA	
18.	WC 21	Upgradation in Mobile Technology and its needs-A Review	Ravneet Kaur Oberai ^{#1} #1 M.Tech student, ECE Department, Guru Nanak Dev Engineering College, Ludhiana, Punjab, INDIA Gurjot Kaur Walia ^{*2} Harminder Kaur ^{*2} ^{*2} Assistant Professor,	

			<i>ECE Department, Guru Nanak Dev Engineering College, Ludhiana, Punjab, INDIA</i> gurjotwalia@yahoo.com, harminder12@yahoo.co.in ravneet.oberai@gmail.com	
19.	WC 22	A NEW APPROACH OF NETWORKING IN MICROWAVE COMMUNICATION SYSTEM	Author1-Er.Sukhjit singh M.tech (ECE) Bgiet,sangrur Email-sukhjit43@gmail.com Author2-Er.Raman Ahuja Mtech ECE UIET,PU(Chandigarh) ahujraman@gmail.com	
20	WC 23	MSEP-E(MODIFIED SEP-E) For Heterogeneous Clustered Wireless Sensor Networks	Simranjit Singh Dept. of Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India Simrangrewal55@gmail.com Kanwaljit Singh Dept. of Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India kjitsingh@ymail.com	
21.	WC 24	Cryptography Algorithms used for data security	Prof. Narwant Singh, Amardeep Singh Guru Nanak Dev Engineering College, Ludhiana	
22.	WC 25	STUDY OF CONGESTION PROBLEM IN WIRELESS SENSOR NETWORKS	Arwinder KaurDept. of Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India arwinder1989@gmail.com Kanwaljit Singh Dept. of Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India kjitsingh@ymail.com	

23.	WC 26	Performance evaluation of IEEE 802.16 (WiMAX) system based Adaptive modulation	Er. Yogesh Garg M.tech Department of ECE Yogesh_273@yahoo.co.in Er. Nirmal Singh Grewal Associate Professor and Head GNE Ludhiana, Punjab nirmaljeet@rediffmail.com Er. Lovekesh Assistant Professor UCoE, Punjabi university lovekesh_bhatia@gmail.com	
24.	WC 27	Intelligent Car Parking System using RFID Technology	Gurveer Kaur ¹ , Amanpreet Kaur ² gurveerkaur59@gmail.com, seerayaman@yahoo.com M. Tech Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana	
25.	WC 29	ENERGY SAVING IN CELLULAR NETWORKS BY FEMTOCELL DEPLOYMENT	Ms. Preet Kaur, Research fellow Ms. Ameeta Seehra, Associate Professor Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India tung.preet@yahoo.com, am_seehra@yahoo.co.in	
26.	WC 30	Energy Efficient Wireless Optical Broadband Access Networks	Arpit Badaisha Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India arpit_badaisha@yahoo.com Ameeta Seehra Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India Am_seehra@yahoo.co.in	
27.	WC 31	Policy Based Service Provisioning System for WiMAX Network	Harminder Kaur ^{#1} , Gurjot Kaur Walia ^{#1} , Jagjeet Kaur ^{#2} , Navandeep Singh Tung ^{#3} #1Assistant Professor, ECE Department, Guru Nanak Dev Engineering College, Ludhiana, Punjab, INDIA #2Assistant Professor, Production Department, Guru Nanak Dev Engineering College, Ludhiana, Punjab, INDIA #3M.Tech Student, UCoE, Punjabi	

			University, Patiala, Punjab, INDIA	
28	WC 32	ANALYSIS OF MODULATION TECHNIQUES USED IN MOBILE WIMAX	Kavita Sharma ¹ , Rupinderjit Kaur ² , Gaurav Soni ³ ^{1,2} M-Tech Scholar, Department of Electronics and Communication Engineering, Amritsar College of Engineering and Technology, Amritsar, India Associate Professor, Department of Electronics and Communication Engineering, Amritsar College of Engineering and Technology, Amritsar, India E-mail: gaurav_soni28@rediffmail.com, kavitasharma809@gmail.com, rupindernandha490@gmail.com	
29.	WC 33	Sensor Deployment Strategies for WSNs	Shivani Dadwal Student, Electronics & Communication Engineering BBSBEC Fatehgarh Sahib, India shivani dadwal@gmail.com Tripatjot Singh Panag Asth. Prof., Electronics & Communication Engineering BBSBEC Fatehgarh Sahib, India panagtripat@gmail.com	

SESSION-2(WIRELESS COMMUNICATION AND SOFT COMPUTING AND OPTIMIZATION)

Sr. No.	Paper ID	Paper Title	Name of authors with contact details	Remarks
1.	SC O 02	Performance of Hypertext Transfer Protocol for Generating Request	Karamjit ¹ , Jasdip Kaur ² , Gurpreet Kaur ³ ^{1,3} Assistant Professor, Department of Electronics and Communication Engineering, ² Lecturer, Department of Electronics and Communication Engineering, ^{1,2,3} North West Institute of Engineering & Technology, Moga, Punjab. email-karmjit9@gmail.com ¹ , email-jasdip.20@gmail.com ² , email- ergupreet30@gmail.com ³	
2.	SCO 03	Improved Method for Managing Password using Blowfish Algorithm	Yogesh Kumar Student, M.TECH ECE Department, Guru Nanak Dev Engg. College, Ludhiana, India, kumar_yogesh1087@rediffmail.c om Kanwaljit Singh Associate Professor ECE Department, Guru Nanak Dev Engg. College Ludhiana, India kjitsingh@gndec.ac.in Daljeet Singh A.P. ECE Department, Lovely Professional University, Punjab, India kumar_yogesh1087@rediffmail.c om	
3.	SCO 04	EMERGING TRENDS OF ELECTRONICS AND COMMUNICATION IN CHEMISTRY LABORATORY-A REVIEW	Dr.Amanpreet Kaur Sodhi A.P. Deptt. Of Applied science. Guru Nanak Dev Engg. College. Ludhiana. Punjab Email : apk_sodhi@yahoo.co.in	
4.	SCO 05	Hybrid Energy- The Future of Energy Sector	Ayush Chandel, NIT, Hamirpur	
5.	SCO 06	PERFORMANCE EVALUATION OF MIMO SYSTEMS WITH DIFFERENT	TanmeetKaur,Balwinder Singh Dhaliwal, Sandeep Singh Gill Department of Electronics and Communication Engineering	

		MODULATION TECHNIQUES	Guru Nanak Dev Engineering College, Ludhiana	
6.	SCO 08	HARDWARE SOLUTION TO REAL TIME PROBLEMS USING CMAC ON FPGA	Varun Prashar: First Author* , Shikha Sharma : CoAuthor** * RIMT Polytechnic/Sr. Lecturer-ECE/Mandi Gobindgarh/India ** RIMT Polytechnic/Lecturer-ECE/Mandi Gobindgarh/India	
7.	SCO 09	REVIEW OF WATER BODY EXTRACTION ALGORITHMS FROM SATELLITE IMAGES	Dr. Lakhwinder Kaur ¹ , Parbhjot Kaur ² 1Prof. Computer Engineering Department, University College of Engineering Punjabi University, Patiala, Punjab, India 2Student of M.Tech Computer Engineering Department, University College of Engineering Punjabi University, Patiala, Punjab, India	
8.	SCO 10	Cybercrime: The Transformation of Crime in the Information Age	Baldip Kaur ¹ , Kamalpreet Kaur ² ¹ Assistant professor in CSE Department, LLRIET Moga (Email: baldip.20@gmail.com) ² Student at University College of Engineering, Punjabi University, Patiala (Email: kamal.khangoora@gmail.com)	
9.	SCO 11	Human Area Networking Using Red Tacton Technology	Er. Varinderjit Kaur ¹ , Er. Talwinder Singh ² , Er. Hardeep Singh Mankoo ³ ^{1,2} Lecturer RIMT Polytechnic College, Mandi Gobindgarh ³ Assistant prof. BGIET, Sangrur mintu_rocks@yahoo.co.in ¹ , talwinder.matharoo@gmail.com ² , er.hardeepmankoo@gmail.com ³	
10.	SCO 14	Various Design Methodology for Industrial Process Controller—A Review	Gurvinder Singh Dept. of electronics and communication Name of organization –	

			G.N.D.E.C Ludhiana(Punjab), India Gurvinder.1312@gmail.com Dr. Sandeep Singh Gill Dept. of Electronics and communication Name of organization – G.N.D.E.C Ludhiana(Punjab), India ssg@gndec.ac.in	
11.	SCO 15	ANTI-LOCK BRAKING SYSTEM AND COLLISION AVOIDANCE1SYSTEM USING FUZZY LOGIC ,	Kulwinder Singh^[1], Assistant Professor, Gulzar Group of Institutes, Khanna,Punjab Ashu Jaggi^[2], Assistant Professor, Gulzar Group of Institutes Khanna,Punjab Rohit Moudgil^[3], Assistant Professor, Gulzar Group of Institutes ,Khanna,Punjab kulwinder87@yahoo.co.in^[1] ashujaggi@gmail.com^[2] rohitmoudgil@ggi.ac.in^[3]	
12.	SCO 16	REVIEW ON DESIGN OF DIGITAL FIR FILTER USING NEURAL NETWORKS	Satinder kaur satinder.459@gmail.com	
13.	SCO 17	COST EFFECTIVENESS AND EFFICIENCY ENHANCEMENT OF A COGENERATION PLANT (A CASE STUDY)	Ravinder Kaur Randhawa*, Gursewak Singh Brar# * Assistant Professor, Electrical Engineering Department, GNDEC, Ludhiana rrandhawa@hotmail.com # Associate Professor, Electrical Engineering Department, BBSBEC, Fatehgarh Sahib Gursewak.singh@bbsbec.ac.in	
14.	SCO 18	Fuzzy Logic Based Traffic Light Controller	Mandeep Kaur Dept. Of E.C.E Student, G.N.D.E.C, Ludhiana, India mandeepkaur1@gmail.com	
15.	SCO 19	A Review on Micro Bacterial Foraging Optimization	Mukta Sharma1, Balwinder Dhaliwal2 1Mtech Student PTU, GNDEC Ludhiana; mukta121@gmail.com Ph-09914005232 2Assistant professor, GNDEC	

			Ludhiana; bsdhaliwal@ymail.com	
16.	SCO 20	Study of Devices Used for Heart Attack Detection	Amandeep Kaur, Nirmaljeet Singh Electronics and Communication Engineering <i>Guru Nanak Dev Engineering College, Ludhiana Punjab, India</i> amandeepkaur25@ymail.com nirmaljeet@rediffmail.com	
17.	SCO 21	Aircraft Take-Off and Landing Using Fuzzy Logics	Amandeep Kaur, Nirmaljeet Singh Electronics and Communication Engineering <i>Guru Nanak Dev Engineering College, Ludhiana Punjab, India</i> amandeepkaur25@ymail.com nirmaljeet@rediffmail.com	
18.	SCO 22	Design and Performance of PID and Fuzzy Logic Controller with Smaller Rule Set for Higher Order System	Rohit Moudgil ^[1] , Assistant Professor, Gulzar Group of Institutes ,Khanna,Punjab Kulwinder Singh ^[2] , Assistant Professor, Gulzar Group of Institutes, Khanna,Punjab Chamanpreet Kaur ^[3] , Assistant Professor, Gulzar Group of Institutes ,Khanna,Punjab rohitmoudgil@ggi.ac.in ^[1] kulwinder87@yahoo.co.in ^[2] Chaman.preet@ggi.ac.in ^[3]	

SESSION-3(OPTICAL FIBER COMMUNICATION)

Sr. No.	Paper ID	Paper Title	Name of authors with contact details	Remarks
1.	OFC 01	Enhancing the Long Haul Systems	Er.Harpreet Kaur ¹ , Er. Rajdeep Kaur ² , Er.Kawalpreet Singh ³ ^{1,2,3} Assistant proff. NWIET Moga harpreetkaur29@gmail.com ¹ , rajdeepkaur_dhaliwal2k@yahoo.com ² , kawal.engg@gmail.com ³	
2.	OFC02	Optical Double sideband signal with different electrical pulses	Er. Jasdip Kaur ¹ , Er. Harpreet Kaur ² , Er. Kawalpreet Singh ³ ^{1,2,3} Assistant proff. NWIET Moga, jasdip.20@gmail.com ¹ , harpreetkaur29@gmail.com ² , kawal.engg@gmail.com ³	
3.	OFC04	CREST FACTOR REDUCTION TECHNIQUES: A REVIEW	Manpreet Kaur ¹ , Er. Harjinder Singh ² ¹ Student, Department of Electronics and Communication Engineering UCoE Patiala-147002, Punjab, India ² Assistant Professor, Department of Electronics and Communication Engineering UCOE Patiala, Punjab, India 1manugandhi04@gmail.com, 2hrjindr@gmail.com	
4.	OFC05	A Review On Various Optical Wireless Technologies And Its Applications	Mandeep Singh, Saurabh Dhawan Students, ECE, GNDU, Amritsar. E-mail: mandeepmatharu99@yahoo.com,saurabhdhawan29@gmail.com	
5.	OFC06	Golay Code as a Peak to Average Power Ratio Reduction Technique	Harleen Kaur*, Amandeep Singh Sappal** (University College of Engineering, Punjabi University, Patiala, India)* (University College of Engineering, Punjabi University, Patiala, India)**	
6./	OFC07	Comparative Study for Reduction of Peak to Average Power Ratio in OFDM System	Rasmeet Kour ¹ , Dr. Amandeep Singh Sappal ² ¹ (ECE, University College of Engineering/ Punjabi University, Patiala) ² (ECE, University College of Engineering/ Punjabi University, Patiala) http://punjabiuniversity.ac.in	
7.	OFC08	Comparison of PS-QPSK with PDM-QPSK and DM-QPSK	Ankja Dubey , Student, GNDEC, Ludhiana	

8.	OFC10	Reduction of Cross Phase Modulation in SCM-WDM System	Ashu Jaggi¹, Baljeet Kaur², Kulwinder Singh³ ¹Student, Department of Electronics & Communication, GNDEC Ludhiana ²Assistant Professor, Department of Electronics & Communication, GNDEC Ludhiana ³Student, Department of Electronics & Communication, GNDEC Ludhiana ¹ashujaggi@ggi.ac.in ²Baleej.kaur@gndec.in ³kulwinder@ggi.ac.in	
9.	OFC 11	INTRODUCTION ON WDM-ROF OPTICAL NETWORK	Shalu (Student) Baljeet Kaur (Assistant Professor) Electronics and communication engineering Electronics and communication engineering Guru Nanak Dev Engineering College, Ludhiana Guru Nanak Dev Engineering College, Ludhiana Punjab, India Shalu.chowdhary@gmail.com baljeetkaur@gndec.ac.in	
10.	OFC12	PERFORMANCE & ANALYSIS OF SOLITONS IN OPTICAL DOMAIN	Neha Gupta¹, Baljeet kaur² ¹Student, G.N.D.E.C, Ludhiana , ²Prof G.N.D.E.C, Ludhiana, ernehaguptaece@gmail.com, baljeetkaur@gndec.ac.in	

11.	OFC 14	An Overview of PAPR and Its Various Reduction Techniques in OFDM systems	Vikas preet tiwana Vikas_tiwana@yahoo.com Electronics & Communication Engineering Punjabi university, Patiala , Punjab, India Sonia singla Electronics & Communication Engineering Punjabi university, Patiala , Punjab, India goyalsonia_82@yahoo.com	
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SESSION-4(SIGNAL PROCESSING)

Sr. No.	Paper ID	Paper Title	Name of authors with contact details	Remarks
1.	SP03	FPGA implementation of CORDIC based Sine/Cosine Digital Wave Generator	Puneet Bansal ¹ , B. S. Dhaliwal ² , S. S. Gill ² ¹ ME Student, ² Faculty, Department of Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab, India puneet4u@gmail.com, bs_dhaliwal@gndec.ac.in, ssg@gndec.ac.in	
2.	SP04	USE OF MATHEMATICAL MODELS IN IMAGE PROCESSING	D.S.Pathania, Department of Mathematics, Guru Nanak Dev Engineering College Ludhiana (Punjab)	
3.	SP05	IMAGE SEGMENTATION AND OBJECT EXTRACTION USING BINARY PARTITION TREE & ANT COLONY OPTIMIZATION	Nancy Garg ¹ , Dr. Munish Rattan ² . 1. Chandigarh Group of Colleges, Landran, e-mail: cecm.ece.ng1@gmail.com 2. Department of ECE, Guru Nanak Dev Engg. College, Ludhiana, e-mail: rattanmunish@gndec.ac.in	
4.	SP 08	Speech Recognition based control of Electrical Appliances	Gaurav Gupta, Gagandeep Singh Faculty of Electronics and Communications Chandigarh Engineering College Landran cecm.ece.gg@gmail.com, cecm.ece.gagan@gmail.com	
5.	SP 09	High Speed ALU Design Using Ancient Vedic Mathematics	Anuradha, Narwant Singh Grewal Electronics and Communication Department Guru Nanak Dev Engineering College Ludhiana, India eranu_85@yahoo.com narwant@gndec.ac.in	
6.	SP 10(A)	Review Article on Wavelet Based Still Image Compression Techniques	Tanupreet Sabharwal ¹ , Munish Rattan ² ^{1,2} Department of Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab, India ¹ cecm.ece.tps@gmail.com , ² rattanmunish@gndec.ac.in	
7.	SP 10(B)	Performance Analysis of Variable	Rashpinder Kaur ¹ , Gurjinder Singh ¹ , Balwinder Singh Dhaliwal ² , Sandeep Singh Gill ² ,	

		Step Size Least Mean Square Algorithms	¹ School of Electronics and Electrical Engineering, Chitkara University, Punjab, India ² Electronics and Communication Engineering Department, Guru Nanak Dev Engineering College, Ludhiana, India (rashpinder.kaur@chitkara.edu.in, gurjinder.singh@chitkara.edu.in, bs_dhaliwal@gndec.ac.in, ssg@gndec.ac.in,)	
8.	SP 11	Design of Low Pass FIR Digital Filter using Genetic Algorithms	Balwinder S. Dhaliwal, Dr. Sandeep Singh Gill ECE Deptt. G.N.D.E.C. Ludhiana. India bs_dhaliwal@gndec.ac.in Chetna M. Tech (ECE) G.N.D.E.C. Ludhiana Ludhiana. India Chetna_jungwal@rediffmail.com	
9.	SP 12	A REVIEW on Different Data Compression Coding Techniques	Amrik Singh and Harjit Singh, M.TECH (ECE-1 st year) S23harjit90@gmail.com	
10.	SP 13	Tunable Digital Filters	Sujata Prabhakar, Dr. Amandeep Singh Sappal UCOE (ECE), Punjabi University, Patiala	
11.	SP 14	Characteristics and Filter Applications of Voltage Controlled Current Conveyor	Susheel Sharma [*] , Rocky Gupta & Vinod Kumar Department of Physics & Electronics, University of Jammu, Jammu-180006 Email: susheelksharma@gmail.com	
12.	SP 15	A Review Paper on Various Level Set Methods	Saloni Pal ¹ , Meena Aggarwal ² , Pushpinder Singh ³ ¹ M.Tech Student, Department of Electronics & Commn. Engg, NIT, Jalandhar ² ME Student, Department of Electronics & Commn. Engg, NITTTR, Chd ³ M.Tech Student, Department of Electronics & Commn. Engg, NIT, Jalandhar	
13.	SP 16	TEXT FUSION IN MEDICAL IMAGING USING ANT COLONY OPTIMIZATION	Er. Shweta Goyal Student Of master's in tech, Dept. of comp. Engg Y.C.o.E, Talwandi Sabo Talwandi Sabo, India Shweta.angel90@gmail.com Er. Gaurav Mangla Student of master's in tech, Dept. of ECE	

			G.K.U,Talwandi sabo Talwandi sabo,India gauravemangla@gmail.com	
14.	SP 17	Review of Linear Phase Low Pass Filter Using Optimization Techniques	Parampal Singh [*] , Er. Balwinder Singh Dhaliwal ^{***} M.Tech-Student(E.C.E), G.N.D.E.C, Ludhiana, INDIA ^{**} Assistant Professor(E.C.E), G.N.D.E.C , Ludhiana, INDIA singhparampal88@gmail.com	
15.	SP 18	BEAM INSTRUMENTATI ON: A TYPICAL APPLICATION OF DIGITAL SIGNAL PROCESSING	Er. Aysha Sharma ¹ , Er. Arashpreet Kaur ² , Er. Jasleen Kaur ³ ^{1,2} Lecturer RIMT Polytechnic College, Mandi Gobindgarh ³ Lecturer Thapar College, Patiala. ayshasharma89@yahoo.com ¹ , arshietian@gmail.com ² , kaur.jas89@yahoo.com ³	
16.	SP 20	Hybrid Differential Evolution Based FIR Filter	Surekha Research scholar Electronics and Communication Engineering Guru Nanak Dev Engineering College	
17.	SP 21	Study of Image Restoration Techniques	Kiran Jot Singh ^{#1} , Nancy Ramanpreet Kaur ^{#2} Electronics & Communication Department, Chandigarh University ¹ kiranjot.693@gmail.com ² nancybadwal@gmail.com	
18.	SP 22	A Quicker Eye to the Future: 3nethra	Chahat Jain ^{#1} , Navneet Kaur ^{*2} , Gurpurneet Kaur ^{#3} , Shyna kalra ^{#4} ^{1,2,3,4} Department of Electronics and Communication, Guru Nanak Dev Engineering College, Ludhiana (Punjab) ¹ chahatjain26@gmail.com	
19.	SP 23	A REVIEW ON VIDEO PROCESSING ARCHITECTURES	Harpreet Singh Cheema Student, M.Tech(Part-Time), GNDEC, Ludhiana, India	
20.	SP 24	A Comparative study of Digital IIR Filter Design using Optimization Techniques	Reetika Singla, Narwant S.Grewal Department of Elect. and Communication Guru Nanak Dev Engineering College, Ludhiana ritu.singla90@ymail.com	

21.	SP 25	Adaptive Filtering Algorithm for Active Noise Control Systems	Kiranbir Kaur, Kulwinder Singh Global Institute of Management and Emerging Technology, Amritsar, Punjab, India Gulzar Group of Institutes, Khanna (Dist. Ludhiana), Punjab, India Kb_kaur@yahoo.com , kulwinder87@yahoo.co.in	
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SESSION-5(EMBEDDED SYSTEM AND MICROWAVE AND ANTENNA)

Sr. No.	Paper ID	Paper Title	Name of authors with contact details	Remarks
1.	MWA 01	A NEW CLASS OF ANTENNAS- SELF STRUCTING ANTENNAS	Gurmukh Singh Dr. Munish Ratan GNDEC, Ludhiana Assistant Professor (Dept. of E.C.E) gurmukh83121@gmail.com rattanmunish@gndec.ac.in G.N.D.E.C	
2.	MWA 02	Implementation of Adaptive Beamformer with FFT for Noise Suppression	Manpreet Singh ¹ , Sandeep Singh Gill ² 1Student M.Tech at Guru Nanak Dev Engineering College, Ludhiana 2Guru Nanak Dev Engineering College, Ludhiana	
3.	MWA 03	Review on Parameter Calculations for Microstrip Patch Antenna Using Various Feeding Methods	Ramna ¹ , Dr. Amandeep Singh Sappal ² ^{1,2} University College of Engineering, Punjabi University, Patiala Email:arora.ramna@gmail.com ¹ ,sappal73as@yahoo.co.in ²	
4.	MWA 04	Patch antenna array and their applications	Manisha Singla Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India Dr. Munish Rattan Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab, India manishasingla.er@gmail.com rattanmunish@gndec.ac.in	
5.	MWA 05	Proximity Coupled Patch Antenna and its Applications	Rupinder Kaur Dr. Munish Rattan Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana Punjab,India Rupinder.k.dakha@gmail.com rattanmunish@gndec.ac.in	
6.	MWA 06	DESIGN OF MULTIBAND C-SHAPED MICROSTRIP PATCH ANTENNA	Lakhwinder Singh [#] , Narwant S. Grewal*, Chahat Jain*, [#] Research scholar, ECE Department, Guru Nanak Dev Engineering College Ludhiana(Punjab),India * Assistant Professor, ECE Department Guru Nanak Dev Engineering College	

7.	MWA 07	Dielectric Constant Measurements of $\text{Ba}_{6-3x}\text{Pr}_{6+2x}\text{Ti}_{18}\text{O}_{54}$ at Microwave Frequencies Range From 8GHz - 12GHz	Harinder kaur Guru Nanak Dev Engg. College, Ludhiana harminder12@yahoo.co.in	
8.	MWA 08	Optimization of Rectangular Microstrip Patch Antenna using Cooperative Bacterial Foraging Optimization	B.S.Dhaliwal ¹ , Richa Sawhney ² ¹ Assistant Professor, ECE Department, GNDEC, Ludhiana, India ² M.Tech Student, ECE Department, GNDEC, Ludhiana, India	

SESSION-5(EMBEDDED SYSTEM AND MICROWAVE AND ANTENNA)

Sr. No.	Paper ID	Paper Title	Name of authors with contact details	Remarks
1.	ES 01	Study of Dynamic Power Management Techniques for Low-Power Embedded Systems	Narender kumar sharmanarender486@gmail.com Dr. Munish Rattan rattanmunish@gndec.ac.in	
2.	ES 02	DESIGN OF AN EMBEDDED SYSTEM THAT DISPLAYS VACANT SEATS IN A BUS	Komal, TaranjotKaur, Nirmal Singh Grewal Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana komal_chand88@yahoo.com taranjotkaur1990@yahoo.com nirmaljeet@rediffmail.com	
3.	ES 03	DESIGN OF AN OBSTACLE DETECTION SYSTEM	Komal, TaranjotKaur, Nirmal Singh Grewal Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana komal_chand88@yahoo.com taranjotkaur1990@yahoo.com nirmaljeet@rediffmail.com	
4.	ES 04	REVIEW ON ROUTING PROTOCOL –EIGRP	Shyna Kalra (Student) Kawaljeet singh (Associate Professor) Chahat Jain(Assistant Professor) Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana enggshyna@gmail.com kjitsingh@gndec.ac.in chahatjain26@gmail.com	
5.	ES 05	Energy Saving using IGBT	Er. Arvind Dhingra ¹ ,Er. Tejinder Singh ²	

6.	ES 06	AUTOMIZATION OF MOBILE ROBOT USING GPS AND UAV	<i>Rashmi</i> Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana <i>rashmi8082@gmail.com</i> <i>Pallavi Jain</i> Electronics & Communication Engineering Guru Nanak Dev Engineering College, Ludhiana <i>er.pallavijain@gmail.com</i>	
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