

To

**TESTING & CONSULTANCY CELL,  
Guru Nanak Dev Engineering College,  
Ludhiana – 141006 (Pb.)**

### **GENERAL INFORMATION**

Guru Nanak Dev Engineering College, Ludhiana is the oldest and a premier Engineering Institution of the state of Punjab. Ludhiana city is a well-known industrial hub in Northern India and is well connected by direct rail service with all parts of India. The beautiful campus is about 7 km away from Railway Station (Auto Rickshaw service available @ Rs.10/- per passenger) and around 6 km from main Bus Stand. If coming by road, then drop at Gill Chowk. Thereafter three-wheelers @ Rs. 10/- per passenger are available for the college. The weather in the month of June in Ludhiana is hot (40<sup>0</sup>C to 44<sup>0</sup>C).

### **HOW TO APPLY**

The participants may send the attached application form or a Xerox copy of the same along with registration fee.

**Six Week Training on**

**ELECTRONIC CIRCUIT  
DESIGN USING VHDL**

**June 01, 2015 to July 13, 2015**



**Head of the Deptt.**

Dr. Sandeep Singh Gill

**Coordinator**

Narwant Singh Grewal

Organized by:

**TESTING & CONSULTANCY CELL,  
ELECTRONICS & COMM. ENGG. DEPTT.**

**Guru Nanak Dev Engineering College,  
Ludhiana, Punjab – 141006**

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## INTRODUCTION

In recent years the study of VLSI technology has become the emerging area in semiconductor electronics. However, trained manpower is scarce in this field. The microscopic dimension of current silicon-integrated circuitry make possible the design of digital circuits which may be very complex, and yet extremely economical in space, power requirements and cost, and potentially very fast. Building a chip requires a variety of CAD tools, both to analyze the design and to synthesize parts of the design. The course concentrates on basic theory of circuit design, its implementation, using VHDL and relevant projects besides industrial exposure.

## COURSE CONTENTS

- Introduction to Digital Circuits
- Comprehensive VHDL introduction
- VHDL Coding styles
- VHDL Test benches & Verification
- Advanced VHDL for synthesis & Verification
- Project

## DEPARTMENT & FACULTY

The Electronics & Communication Engg. department is NBA accredited and runs both UG & PG courses. It has state of the art facilities for research & development. The faculty for the course shall include our own faculty. Experts drawn from other Academic Institutions & Industry shall be invited to deliver expert lectures.

## ELIGIBILITY CRITERIA

Students of 2nd year Electronics/ Computer Science/ Information Technology/Electrical shall be eligible to apply for participation.

## REGISTRATION

A registration Fee of Rs.5000 + service tax is to be paid by cash or demand draft in favour of "Director, Guru Nanak Dev Engineering College, Ludhiana", payable at Ludhiana. The cash payment can be made on any working day in the Testing & Consultancy Cell of the College.

## BOARDING & LODGING

The participants shall be provided with subsidized boarding & lodging in the students hostel on prior request.

## Six Weeks Training on Electronic Circuit Design using VHDL (June 01, 2015 to July 13, 2015)

### APPLICATION FORM

Name : .....

Father's Name : .....

Date of Birth : .....

Name of the College : .....

Univ. Roll No. : .....

Mailing Address : .....

Mobile No. : .....

E-Mail : .....

Bank Draft No./Cash : .....

Accommodation required : Yes/No

Please register me for the above course.

Signature of  
Applicant