

## CONTACT

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## GENERAL INFORMATION

Guru Nanak Dev Engineering College, Ludhiana is the oldest and a premier Engineering Institute of India. It is an autonomous college under UGC Act 1956 [2(f) and 12(B)]. The Institute is setup on 88 acres of sprawling pristine land on Ludhiana-Malerkotla state highway. Ludhiana city is a well known Industrial hub in Northern India and is well connected by direct rail service with all parts of India. The beautiful campus is about 7 kms away from railway station . The weather in the month of July in Ludhiana is hot with temperature around 42°C.

## REGISTRATION

There is no Registration fee for the participants from academic Institutes. Participants from industry/ field shall have to pay registration fee of Rs. 5,000/-

**TEQIP-II Sponsored  
Faculty Development Programme  
on  
“Technology and Management  
for Electronic Product Design”  
(May 2, 2016 to May 13, 2016)**



**Chief Coordinator**

**Dr. Sandeep Singh Gill**

**Coordinators**

**Dr. Munish Rattan  
Prof. Narwant Singh Grewal**

**Organized by  
Department of Electronics & Communication  
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GURU NANAK DEV ENGINEERING  
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Ludhiana, Punjab - 141006  
Phone No. 0161-5064765  
E-mail: [gne.ecefdp@gmail.com](mailto:gne.ecefdp@gmail.com)**

## INTRODUCTION

With Indian manufacturing sector in general, and electronics industry in particular, set for a boom due to initiatives taken by government such as start up India, the knowledge regarding the subject of product design has become highly relevant for academicians, engineers as well as aspiring entrepreneurs. The electronic product design and management has become a very crucial process which needs to be considered for the long-term roadmap of the product in the dynamic market. The process of electronic product design requires a logical progression from a functional specification to the finished product.

The functional specifications come from the analysis of the needs and expectations of the product which can be expressed as a set of design parameters. The course aims to impact advanced knowledge and skills in the electronic product design and associated management issues.

## COURSE CONTENTS

- ✓ Process of electronic product design
- ✓ Need and Formulation of functional specifications, Feasibility study, Design, Prototype, Testing, Design Review, Documentation, Follow –up etc.
- ✓ Embedded systems
- ✓ Advanced controllers, Programming and interfacing
- ✓ FPGA based system design
- ✓ Silicon on Chip
- ✓ Antenna On chip
- ✓ Manufacturing Technology
- ✓ CAD/CAM.
- ✓ 3-D printing
- ✓ Reliability Engineering

## FACULTY

Experts from IITs, NITs and other renowned academic Institutions besides faculty from our institute will deliver expert talks.

## ELIGIBILITY CRITERIA

The program is interdisciplinary in nature and has been designed for all Engineering, Sciences & Management Faculty, professionals, practicing Engineers, and PG students.

## BOARDING & LODGING

Accommodation shall be provided in college hostels free of charge. However, guest house/ hotel/lodge may be booked on payment basis if intimated well in advance.

## IMPORTANT DATES

- Last date for Receiving Application: April 20, 2016
- Notification of Acceptance: April 25, 2016

## TEQIP-II Sponsored FDP on

“Technology and Management for Electronic Product Design”

(May 2, 2016 to May 13, 2016)

## APPLICATION FORM

Name: .....

Father's Name: .....

Date of Birth: .....

Academic Qualification:.....

Designation:.....

Address of Sponsoring Authority:.....

.....

Experience (years) : .....

Mobile No.....

E-Mail : .....

Accommodation required: Yes/No

Date:

Signature of Applicant

## SPONSORSHIP CERTIFICATE

The applicant will be permitted to participate in the above programme, if selected.

Signature of Sponsoring authority